# **Another times Macro Instruction**

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**Summary.** The semantics of the times macro is given in [2] only for the case when the body of the macro is parahalting. We remedy this by defining a new times macro instruction in terms of while (see [11], [14]). The semantics of the new times macro is given in a way analogous to the semantics of while macros. The new times uses an anonymous variable to control the number of its executions. We present two examples: a trivial one and a remake of the macro for the Fibonacci sequence (see [13]).

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The articles [16], [21], [17], [6], [5], [22], [8], [9], [10], [7], [12], [18], [20], [19], [3], [15], [4], [1], [11], and [13] provide the notation and terminology for this paper.

### 1. **SCM**<sub>FSA</sub> PRELIMINARIES

For simplicity, we follow the rules: s,  $s_1$ ,  $s_2$  denote states of  $\mathbf{SCM}_{FSA}$ , a, b denote integer locations, d denotes a read-write integer location, f denotes a finite sequence location, f denotes a macro instruction, f denotes a good macro instruction, and f denotes a natural number.

One can prove the following propositions:

- (1) If I is closed on Initialize(s) and halting on Initialize(s) and  $b \notin UsedIntLoc(I)$ , then (IExec(I,s))(b) = (Initialize(s))(b).
- (2) If I is closed on Initialize(s) and halting on Initialize(s) and  $f \notin UsedInt^*Loc(I)$ , then (IExec(I,s))(f) = (Initialize(s))(f).
- (3) Suppose I is closed on Initialize(s), halting on Initialize(s), and parahalting but  $s(\operatorname{intloc}(0)) = 1$  or a is read-write but  $a \notin \operatorname{UsedIntLoc}(I)$ . Then  $(\operatorname{IExec}(I,s))(a) = s(a)$ .
- (4) If s(intloc(0)) = 1, then I is closed on s iff I is closed on Initialize(s).
- (5) If s(intloc(0)) = 1, then I is closed on s and halting on s iff I is closed on Initialize(s) and halting on Initialize(s).
- (6) Let  $I_1$  be a subset of Int-Locations and  $F_1$  be a subset of FinSeq-Locations. Then  $s_1 \upharpoonright (I_1 \cup F_1) = s_2 \upharpoonright (I_1 \cup F_1)$  if and only if the following conditions are satisfied:
- (i) for every integer location x such that  $x \in I_1$  holds  $s_1(x) = s_2(x)$ , and
- (ii) for every finite sequence location x such that  $x \in F_1$  holds  $s_1(x) = s_2(x)$ .

- (7) Let  $I_1$  be a subset of Int-Locations. Then  $s_1 \upharpoonright (I_1 \cup \text{FinSeq-Locations}) = s_2 \upharpoonright (I_1 \cup \text{FinSeq-Locations})$  if and only if the following conditions are satisfied:
- (i) for every integer location x such that  $x \in I_1$  holds  $s_1(x) = s_2(x)$ , and
- (ii) for every finite sequence location x holds  $s_1(x) = s_2(x)$ .

#### 2. ANOTHER times MACRO INSTRUCTION

Let a be an integer location and let I be a macro instruction. The functor times(a,I) yielding a macro instruction is defined as follows:

(Def. 1)  $\operatorname{times}(a, I) = (a_1 := a);$  (while  $a_1 > 0$  do  $(I; \operatorname{SubFrom}(a_1, \operatorname{intloc}(0)))),$  where  $a_1 = 1^{\operatorname{st}} - \operatorname{RWNotIn}(\{a\} \cup \operatorname{UsedIntLoc}(I)).$ 

We introduce a times I as a synonym of times (a, I).

We now state two propositions:

- (8)  $\{b\} \cup \text{UsedIntLoc}(I) \subseteq \text{UsedIntLoc}(\text{times}(b, I)).$
- (9)  $UsedInt^*Loc(times(b, I)) = UsedInt^*Loc(I)$ .

Let I be a good macro instruction and let a be an integer location. Observe that times(a, I) is good.

Let s be a state of  $\mathbf{SCM}_{FSA}$ , let I be a macro instruction, and let a be an integer location. The functor StepTimes(a, I, s) yielding a function from  $\mathbb{N}$  into  $\prod$  (the object kind of  $\mathbf{SCM}_{FSA}$ ) is defined as follows:

(Def. 2) StepTimes $(a, I, s) = StepWhile > O(a_1, I; SubFrom(a_1, intloc(0)), Exec(a_1 := a, Initialize(s))),$  where  $a_1 = 1^{st}$  -RWNotIn( $\{a\} \cup UsedIntLoc(I)\}$ ).

Next we state several propositions:

- (10) (StepTimes(a,J,s))(0)(intloc(0)) = 1.
- (11) If  $s(\operatorname{intloc}(0)) = 1$  or a is read-write, then  $(\operatorname{StepTimes}(a, J, s))(0)(1^{\operatorname{st}} \operatorname{RWNotIn}(\{a\} \cup \operatorname{UsedIntLoc}(J))) = s(a)$ .
- (12) Suppose (StepTimes(a,J,s))(k)(intloc(0)) = 1 and J is closed on (StepTimes(a,J,s))(k) and halting on (StepTimes(a,J,s))(k). Then (StepTimes(a,J,s))(k+1)(intloc(0)) = 1 and if  $(StepTimes(a,J,s))(k)(1^{st}-RWNotIn(\{a\}\cup UsedIntLoc(J))) > 0$ , then  $(StepTimes(a,J,s))(k+1)(1^{st}-RWNotIn(\{a\}\cup UsedIntLoc(J))) = (StepTimes(a,J,s))(k)(1^{st}-RWNotIn(\{a\}\cup UsedIntLoc(J))) 1$ .
- (13) If s(intloc(0)) = 1 or a is read-write, then (StepTimes(a, I, s))(0)(a) = s(a).
- (14) (StepTimes(a, I, s))(0)(f) = s(f).

Let s be a state of  $\mathbf{SCM}_{FSA}$ , let a be an integer location, and let I be a macro instruction. We say that ProperTimesBody a, I, s if and only if:

(Def. 3) For every natural number k such that k < s(a) holds I is closed on (StepTimes(a,I,s))(k) and halting on (StepTimes(a,I,s))(k).

One can prove the following propositions:

- (15) If *I* is parahalting, then ProperTimesBody *a*, *I*, *s*.
- (16) If ProperTimesBody a, J, s, then for every k such that  $k \le s(a)$  holds (StepTimes(a, J, s))(k)(intloc(0)) = 1.
- (17) Suppose s(intloc(0)) = 1 or a is read-write but ProperTimesBody a, J, s. Let given k. If  $k \le s(a)$ , then  $(\text{StepTimes}(a, J, s))(k)(1^{\text{st}} \text{RWNotIn}(\{a\} \cup \text{UsedIntLoc}(J))) + k = s(a)$ .

- (18) Suppose ProperTimesBody a, J, s but  $0 \le s(a)$  but  $s(\operatorname{intloc}(0)) = 1$  or a is read-write. Let given k. If  $k \ge s(a)$ , then  $(\operatorname{StepTimes}(a,J,s))(k)(1^{\operatorname{st}}-\operatorname{RWNotIn}(\{a\} \cup \operatorname{UsedIntLoc}(J))) = 0$  and  $(\operatorname{StepTimes}(a,J,s))(k)(\operatorname{intloc}(0)) = 1$ .
- (19) If s(intloc(0)) = 1, then  $(\text{StepTimes}(a, I, s))(0) \upharpoonright (\text{UsedIntLoc}(I) \cup \text{FinSeq-Locations}) = s \upharpoonright (\text{UsedIntLoc}(I) \cup \text{FinSeq-Locations})$ .
- (20) Suppose (StepTimes(a,J,s))(k)(intloc(0)) = 1 and J is halting on Initialize((StepTimes(a,J,s))(k)) and closed on Initialize((StepTimes(a,J,s))(k)) and (StepTimes(a,J,s))(k)(1st -RWNotIn( $\{a\}$   $\cup$  UsedIntLoc(J))) > 0. Then (StepTimes(a,J,s))(k+1)†(UsedIntLoc(J)  $\cup$  FinSeq-Locations) = IExec(J, (StepTimes(a,J,s))(k))†(UsedIntLoc(J)  $\cup$  FinSeq-Locations).
- (21) Suppose ProperTimesBody a, J, s or J is parahalting but k < s(a) but s(intloc(0)) = 1 or a is read-write. Then  $(\text{StepTimes}(a,J,s))(k+1) \upharpoonright (\text{UsedIntLoc}(J) \cup \text{FinSeq-Locations}) = \text{IExec}(J, (\text{StepTimes}(a,J,s))(k)) \upharpoonright (\text{UsedIntLoc}(J) \cup \text{FinSeq-Locations}).$
- (22) If  $s(a) \le 0$  and  $s(\operatorname{intloc}(0)) = 1$ , then  $\operatorname{IExec}(\operatorname{times}(a, I), s) \upharpoonright (\operatorname{UsedIntLoc}(I) \cup \operatorname{FinSeq-Locations}) = s \upharpoonright (\operatorname{UsedIntLoc}(I) \cup \operatorname{FinSeq-Locations})$ .
- (23) Suppose s(a) = k but ProperTimesBody a, J, s or J is parahalting but  $s(\operatorname{intloc}(0)) = 1$  or a is read-write. Then  $\operatorname{IExec}(\operatorname{times}(a,J),s) \upharpoonright D = (\operatorname{StepTimes}(a,J,s))(k) \upharpoonright D$ , where  $D = \operatorname{Int-Locations} \cup \operatorname{FinSeq-Locations}$ .
- (24) If s(intloc(0)) = 1 and if ProperTimesBody a, J, s or J is parahalting, then times(a,J) is closed on s and times(a,J) is halting on s.

#### 3. A TRIVIAL EXAMPLE

Let d be a read-write integer location. The functor triv-times(d) yields a macro instruction and is defined by:

(Def. 4) triv-times(d) = times(d, (while d = 0 do Macro(d:=d)); SubFrom(d, intloc(0))).

We now state two propositions:

- (25) If  $s(d) \le 0$ , then (IExec(triv-times(d), s))(d) = s(d).
- (26) If  $0 \le s(d)$ , then (IExec(triv-times(d), s))(d) = 0.

### 4. A MACRO FOR THE FIBONACCI SEQUENCE

Let N,  $r_1$  be integer locations. The functor Fib-macro $(N, r_1)$  yielding a macro instruction is defined as follows:

(Def. 5) Fib-macro( $N, r_1$ ) = ( $N_1$ :=N); SubFrom( $r_1, r_1$ ); ( $n_1$ :=intloc(0)); times( $N, AddTo(r_1, n_1)$ ; swap( $n_1$ ); (N:= $N_1$ ), where  $N_1 = 1^{st}$ -NotUsed(times( $N, AddTo(r_1, n_1)$ ); swap( $n_1$ )) and  $n_1 = 1^{st}$ -RWNotIn( $n_1$ ).

Next we state the proposition

(27) Let N,  $r_1$  be read-write integer locations. Suppose  $N \neq r_1$ . Let n be a natural number. If n = s(N), then  $(\text{IExec}(\text{Fib-macro}(N, r_1), s))(r_1) = \text{Fib}(n)$  and  $(\text{IExec}(\text{Fib-macro}(N, r_1), s))(N) = s(N)$ .

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