

The Basic Properties of SCM over Ring

Artur Korniłowicz
University of Białystok

MML Identifier: SCMRING2.

The articles [6], [7], [12], [1], [8], [2], [3], [10], [4], [11], [9], and [5] provide the terminology and notation for this paper.

1. SCM OVER RING

In this paper I is an element of \mathbb{Z}_8 , S is a non empty 1-sorted structure, t is an element of the carrier of S , and x is a set.

Let R be a good ring. The functor $\mathbf{SCM}(R)$ yields a strict AMI over {the carrier of R } and is defined by the conditions (Def. 1).

- (Def. 1)(i) The objects of $\mathbf{SCM}(R) = \mathbb{N}$,
- (ii) the instruction counter of $\mathbf{SCM}(R) = 0$,
 - (iii) the instruction locations of $\mathbf{SCM}(R) = \text{Instr-Loc}_{\mathbf{SCM}}$,
 - (iv) the instruction codes of $\mathbf{SCM}(R) = \mathbb{Z}_8$,
 - (v) the instructions of $\mathbf{SCM}(R) = \text{Instr}_{\mathbf{SCM}(R)}$,
 - (vi) the object kind of $\mathbf{SCM}(R) = \text{OK}_{\mathbf{SCM}(R)}$, and
 - (vii) the execution of $\mathbf{SCM}(R) = \text{Exec}_{\mathbf{SCM}(R)}$.

Let R be a good ring, let s be a state of $\mathbf{SCM}(R)$, and let a be an element of $\text{Data-Loc}_{\mathbf{SCM}}$. Then $s(a)$ is an element of R .

Let R be a good ring. An object of $\mathbf{SCM}(R)$ is called a Data-Location of R if:

- (Def. 2) $It \in (\text{the objects of } \mathbf{SCM}(R)) \setminus (\text{Instr-Loc}_{\mathbf{SCM}} \cup \{0\})$.

For simplicity, we use the following convention: R is a good ring, r is an element of the carrier of R , a, b, c, d_1, d_2 are Data-Location of R , and i_1 is an instruction-location of $\mathbf{SCM}(R)$.

Next we state the proposition

- (1) x is a Data-Location of R iff $x \in \text{Data-Loc}_{\text{SCM}}$.

Let R be a good ring, let s be a state of $\mathbf{SCM}(R)$, and let a be a Data-Location of R . Then $s(a)$ is an element of R .

We now state several propositions:

- (2) $\langle 0, \varepsilon \rangle \in \text{Instr}_{\text{SCM}}(S)$.
(3) $\langle 0, \varepsilon \rangle$ is an instruction of $\mathbf{SCM}(R)$.
(4) If $x \in \{1, 2, 3, 4\}$, then $\langle x, \langle d_1, d_2 \rangle \rangle \in \text{Instr}_{\text{SCM}}(S)$.
(5) $\langle 5, \langle d_1, t \rangle \rangle \in \text{Instr}_{\text{SCM}}(S)$.
(6) $\langle 6, \langle i_1 \rangle \rangle \in \text{Instr}_{\text{SCM}}(S)$.
(7) $\langle 7, \langle i_1, d_1 \rangle \rangle \in \text{Instr}_{\text{SCM}}(S)$.

Let R be a good ring and let a, b be Data-Location of R . The functor $a:=b$ yielding an instruction of $\mathbf{SCM}(R)$ is defined by:

- (Def. 3) $a:=b = \langle 1, \langle a, b \rangle \rangle$.

The functor $\text{AddTo}(a, b)$ yielding an instruction of $\mathbf{SCM}(R)$ is defined by:

- (Def. 4) $\text{AddTo}(a, b) = \langle 2, \langle a, b \rangle \rangle$.

The functor $\text{SubFrom}(a, b)$ yielding an instruction of $\mathbf{SCM}(R)$ is defined by:

- (Def. 5) $\text{SubFrom}(a, b) = \langle 3, \langle a, b \rangle \rangle$.

The functor $\text{MultBy}(a, b)$ yielding an instruction of $\mathbf{SCM}(R)$ is defined as follows:

- (Def. 6) $\text{MultBy}(a, b) = \langle 4, \langle a, b \rangle \rangle$.

Let R be a good ring, let a be a Data-Location of R , and let r be an element of the carrier of R . The functor $a:=r$ yields an instruction of $\mathbf{SCM}(R)$ and is defined by:

- (Def. 7) $a:=r = \langle 5, \langle a, r \rangle \rangle$.

Let R be a good ring and let l be an instruction-location of $\mathbf{SCM}(R)$. The functor $\text{goto } l$ yielding an instruction of $\mathbf{SCM}(R)$ is defined by:

- (Def. 8) $\text{goto } l = \langle 6, \langle l \rangle \rangle$.

Let R be a good ring, let l be an instruction-location of $\mathbf{SCM}(R)$, and let a be a Data-Location of R . The functor **if** $a = 0$ **goto** l yielding an instruction of $\mathbf{SCM}(R)$ is defined as follows:

- (Def. 9) **if** $a = 0$ **goto** $l = \langle 7, \langle l, a \rangle \rangle$.

One can prove the following proposition

- (8) Let I be a set. Then I is an instruction of $\mathbf{SCM}(R)$ if and only if one of the following conditions is satisfied:
- (i) $I = \langle 0, \varepsilon \rangle$, or
 - (ii) there exist a, b such that $I = a:=b$, or
 - (iii) there exist a, b such that $I = \text{AddTo}(a, b)$, or
 - (iv) there exist a, b such that $I = \text{SubFrom}(a, b)$, or

- (v) there exist a, b such that $I = \text{MultBy}(a, b)$, or
- (vi) there exists i_1 such that $I = \text{goto } i_1$, or
- (vii) there exist a, i_1 such that $I = \text{if } a = 0 \text{ goto } i_1$, or
- (viii) there exist a, r such that $I = a := r$.

In the sequel s denotes a state of **SCM**(R).

Let us consider R . Observe that **SCM**(R) is von Neumann.

The following two propositions are true:

- (9) $\mathbf{IC}_{\mathbf{SCM}(R)} = 0$.
- (10) For every **SCM**-state S over R such that $S = s$ holds $\mathbf{IC}_s = \mathbf{IC}_S$.

Let R be a good ring and let i_1 be an instruction-location of **SCM**(R). The functor $\text{Next}(i_1)$ yields an instruction-location of **SCM**(R) and is defined by:

- (Def. 10) There exists an element m_1 of $\text{Instr-Loc}_{\mathbf{SCM}}$ such that $m_1 = i_1$ and $\text{Next}(i_1) = \text{Next}(m_1)$.

The following propositions are true:

- (11) For every instruction-location i_1 of **SCM**(R) and for every element m_1 of $\text{Instr-Loc}_{\mathbf{SCM}}$ such that $m_1 = i_1$ holds $\text{Next}(m_1) = \text{Next}(i_1)$.
- (12) Let I be an instruction of **SCM**(R) and i be an element of $\text{Instr}_{\mathbf{SCM}(R)}$. If $i = I$, then for every **SCM**-state S over R such that $S = s$ holds $\text{Exec}(I, s) = \text{Exec-Res}_{\mathbf{SCM}}(i, S)$.

2. USERS GUIDE

Next we state several propositions:

- (13) $(\text{Exec}(a := b, s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(a := b, s))(a) = s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(a := b, s))(c) = s(c)$.
- (14) $(\text{Exec}(\text{AddTo}(a, b), s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{AddTo}(a, b), s))(a) = s(a) + s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(\text{AddTo}(a, b), s))(c) = s(c)$.
- (15) $(\text{Exec}(\text{SubFrom}(a, b), s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{SubFrom}(a, b), s))(a) = s(a) - s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(\text{SubFrom}(a, b), s))(c) = s(c)$.
- (16) $(\text{Exec}(\text{MultBy}(a, b), s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{MultBy}(a, b), s))(a) = s(a) \cdot s(b)$ and for every c such that $c \neq a$ holds $(\text{Exec}(\text{MultBy}(a, b), s))(c) = s(c)$.
- (17) $(\text{Exec}(\text{goto } i_1, s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = i_1$ and $(\text{Exec}(\text{goto } i_1, s))(c) = s(c)$.
- (18) If $s(a) = 0_R$, then $(\text{Exec}(\text{if } a = 0 \text{ goto } i_1, s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = i_1$ and if $s(a) \neq 0_R$, then $(\text{Exec}(\text{if } a = 0 \text{ goto } i_1, s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(\text{if } a = 0 \text{ goto } i_1, s))(c) = s(c)$.

- (19) $(\text{Exec}(a:=r, s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = \text{Next}(\mathbf{IC}_s)$ and $(\text{Exec}(a:=r, s))(a) = r$
and for every c such that $c \neq a$ holds $(\text{Exec}(a:=r, s))(c) = s(c)$.

3. HALT INSTRUCTION

The following two propositions are true:

- (20) For every instruction I of $\mathbf{SCM}(R)$ such that there exists s such that
 $(\text{Exec}(I, s))(\mathbf{IC}_{\mathbf{SCM}(R)}) = \text{Next}(\mathbf{IC}_s)$ holds I is non halting.
(21) For every instruction I of $\mathbf{SCM}(R)$ such that $I = \langle 0, \varepsilon \rangle$ holds I is
halting.

Let us consider R, a, b . One can check the following observations:

- * $a:=b$ is non halting,
- * $\text{AddTo}(a, b)$ is non halting,
- * $\text{SubFrom}(a, b)$ is non halting, and
- * $\text{MultBy}(a, b)$ is non halting.

Let us consider R, i_1 . Observe that $\text{goto } i_1$ is non halting.

Let us consider R, a, i_1 . Observe that **if** $a = 0$ **goto** i_1 is non halting.

Let us consider R, a, r . Note that $a:=r$ is non halting.

Let us consider R . One can check that $\mathbf{SCM}(R)$ is halting definite data-oriented steady-programmed and realistic.

One can prove the following propositions:

- (29)¹ For every instruction I of $\mathbf{SCM}(R)$ such that I is halting holds $I =$
 $\mathbf{halt}_{\mathbf{SCM}(R)}$.
(30) $\mathbf{halt}_{\mathbf{SCM}(R)} = \langle 0, \varepsilon \rangle$.

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¹The propositions (22)–(28) have been removed.

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Received November 29, 1998
